

Subject Code: 17323 (ECN)

Important Instructions to examiners:

1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.

2) The model answer and the answer written by candidate may vary but the examiner should assess the understanding level of the candidate.

3) The language errors such as grammatical, spelling errors should not be given importance (Not applicable for subject English and Communication Skills).

4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner should give credit for any equivalent figure/figures drawn.

5) Credits to be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer (as long as the assumptions are not incorrect).

6) In case of some questions credit may be given by judgment on part of examiner of relevant answer based on candidate's understanding.

7) For programming language papers, credit may be given to any other program based on equivalent concept

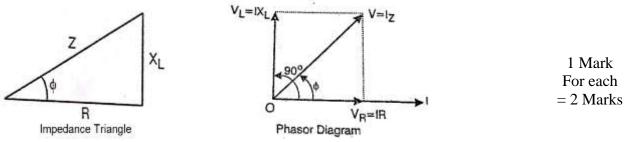


1 1 a)

1b)

1c)

	Winter – 2017 Examinations Model AnswerSubject Code: 17	7323 (ECN)
Atter	npt any <u>TEN</u> of the following:	20
Defin	e cycle and time period related to a. c. waveform.	
Ans:		
i)	Cycle:	
	A complete set of variation of an alternating quantity which is repeated at regular interval of time is called as a cycle. OR	1 Mark
ii)	Each repetition of an alternating quantity recurring at equal intervals is known as a cycle. Time Period:	
11)	Time period of an alternating quantity is defined as the time required for an alternating quantity to complete one cycle.	1 Mark
Defin	e active power and reactive power for R-L-C series circuit.	
Ans:		
	ve power and reactive power for R-L-C series circuit:	
(i)	Active Power (P):	
	Active power (P) is given by the product of voltage, current and the	
	cosine of the phase angle between voltage and current.	1 Mark
	Unit: watt (W) or kilo-watt (kW) or Mega-watt (MW)	
(ii)	$P = VIcos \emptyset = I^2 R watt$	
(11)	Reactive Power (Q): Reactive power (Q) is given by the product of voltage, current and the sine of the phase angle between voltage and current.	
	Unit: volt-ampere-reactive (VAr), or kilo-volt-ampere-reactive (kVAr) or Mega-volt-ampere-reactive (MVAr) $Q = VIsin\phi = I^2X$ volt-amp-reactive	1 Mark
Dearro		
Draw Ans:	r impedance triangle and voltage phasor diagram for R-L series circuits.	
	$V_1 = X_1 $	



1 d) Define susceptance and admittances for a parallel circuit. Ans:

Susceptance (B):

Susceptance is defined as the imaginary part of the admittance.

It is expressed as, $B = \frac{X}{R^2 + X^2}$

In DC circuit, the reactance is absent, hence X = 0 and susceptance becomes equal to zero.

Admittance (Y):

Admittance is defined as the ability of the circuit to carry (admit) alternating 1 Mark



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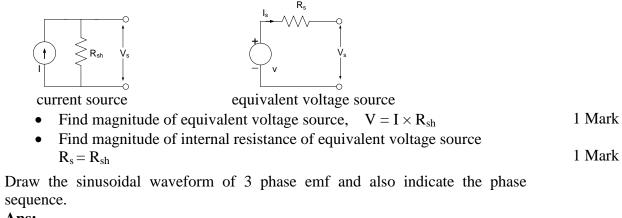
current through it. It is the reciprocal of impedance Z. i.e Y = 1/Z. If impedance is expressed as $Z = R \pm jX$, then the admittance is obtained as, (Equations are $Y = \frac{1}{Z} = \frac{1}{R \pm jX} = \frac{R \mp jX}{(R + jX)(R - jX)} = \frac{R \mp jX}{R^2 + X^2}$ $\therefore Y = \frac{R}{R^2 + X^2} \mp j\frac{X}{R^2 + X^2} = G \mp jB$ optional) State superposition theorem applied to D.C. circuits. 1eAns: Superposition Theorem applied to D.C. circuits: Superposition theorem states that in any linear, bilateral, multisource network, the response (voltage across any element or current through any 2 Marks element) of any branch is equal to the algebraic sum of the responses produced in it with each source acting alone, while the other sources are replaced by their internal resistances. OR Any other valid statement 1f) State maximum power transfer theorem for DC circuit. Ans: Maximum power transfertheorem for DC circuit: The maximum power transfer theorem states that the source or a network transfers maximum power to load only when the load resistance is equal to the internal resistance of the source or the network. 2 Marks The internal resistance of the network is the Thevenin equivalent resistance of the network seen between the terminals at which the load is connected when: i) The load is removed (disconnected) ii) All internal independent sources are replaced by their internal resistances. Write down the units of R, L, C and G 1g) Ans: ¹/₂ mark each Units of R, L, C and G: R - ohm L – henry C – farads G - siemens or mho =2 Marks 1h) Define Quality factor of series AC circuit. Ans: **Ouality Factor of Series AC circuit:** The quality factor basically represents a figure of merit of a component (practical inductor or capacitor) or a complete circuit. It is a dimensionless number and defined as: $Q = 2\pi \left[\frac{Maximum\ energy\ stored}{Energy\ dissipated\ per\ cycle} \right]$ 2 Marks In series circuit it is defined as voltage magnification in the circuit at resonance OR It is also defined as the ratio of the reactive power of either the inductor or the capacitor to the average power of the resistor. $Q \text{ factor} = \text{voltage magnification} = \frac{1}{R} \sqrt{\frac{L}{C}}$



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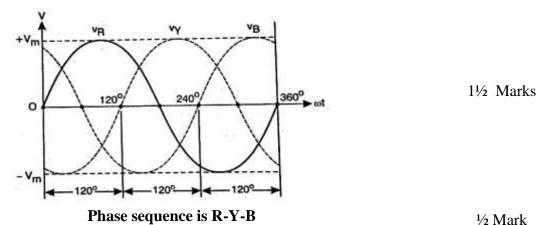
1i) How current source can be converted into equivalent voltage source? Ans:

Conversion of current source into equivalent voltage source:

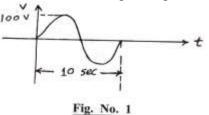


1 j)

Ans:



1 k) Find frequency and RMS value of following voltage waveform refer Fig. No.1



Ans:

Frequency (f)
$$= \frac{1}{T} = \frac{1}{10} = 0.1$$
Hz 1 Mark

RMS Value V =
$$\frac{V_{\text{peak}}}{\sqrt{2}} = 0.707 \times V_{\text{peak}} = 0.707 \times 100 = 70.7 \text{ V}$$
 1 Mark

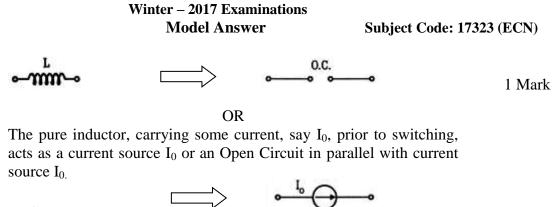
State the behavior of following elements at the time of switching i.e. transient 11)period. - i) Pure L ii) Pure C Ans:

Behavior of pure L at the time of switching i.e. transient period:

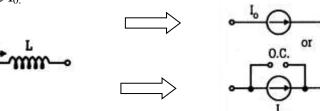
The pure inductor, carrying zero current prior to switching, acts as i) OPEN CIRCUIT.



ii)



S.C.



Behavior of pure C at the time of switching i.e.transient period:

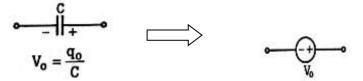
i) The pure capacitor, having zero voltage prior to switching, acts as SHORT CIRCUIT.



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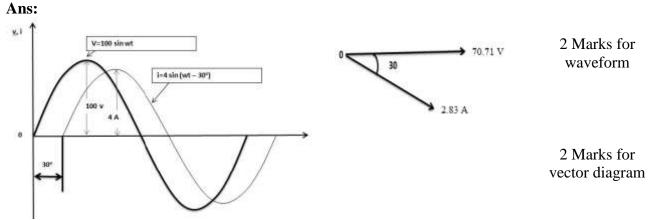
ii) The pure capacitor, having some voltage, say V0, prior to switching, acts as a voltage source V0 or Short Circuit in series with voltage source V0.



2 Attempt any <u>FOUR</u> of the following:

2a) Draw waveform and vector diagram to show following voltage and current.

 $V = 100 \sin wt$, and $I = 4 \sin (wt - 30^\circ)$



In vector diagram, RMS values have been shown.

2b) Compare series and parallel circuits on any six points. Ans:



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Sr.	urison between Series and Parallel Series Circuit	Parallel Circuit	
No.			
1	$ \begin{array}{c} $	$\begin{array}{c} & & & \\ & & & \\ + & & & \\ - & & & \\ \end{array} \begin{array}{c} & & & \\ & & & \\ \end{array} \end{array}$	
2	A series circuit is that circuit in which the current flowing through each circuit element is same.	A parallel circuit is that circuit in which the voltage across each circuit element is same.	
3	The sum of the voltage drops in series resistances is equal to the applied voltage V. \therefore V = V ₁ +V ₂ +V ₃	The sum of the currents in parallel resistances is equal to the total circuit current I. $\therefore I = I_1 + I_2 + I_3$	
4	$\therefore V = V_1 + V_2 + V_3$ The effective resistance R of the series circuit is the sum of the individual resistances connected in series. $R = R_1 + R_2 + R_3 + \cdots$	The reciprocal of effective resistance R of the parallel circuit is the sum of the reciprocals of the individual resistances connected in parallel. $\frac{1}{R} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \cdots$	
5	For series R-L-C circuit, the resonance frequency is, $f_r = \frac{1}{2\pi\sqrt{LC}}$	For parallel R-L-C circuit, the resonance frequency is, $f_r = \frac{1}{2\pi\sqrt{LC}}$	
6	At resonance, the series RLC circuit behaves as purely resistive circuit.	At resonance, the parallel RLC circuit behaves as purely resistive circuit.	
7	At resonance, the series RLC circuit power factor is unity.	At resonance, the Parallel RLC circuit power factor is unity.	
8	At resonance, the series RLC circuit offers minimum total impedance $Z = R$	At resonance, the parallel RLC circuit offers maximum total impedance $Z = L/CR$	
9	At resonance, series RLC circuit draws maximum current from source, $I = (V/R)$	At resonance, parallel RLC circuit draws minimum current from source, $I = \frac{V}{[L/CR]}$	
10	At resonance, in series RLC circuit, voltage magnification takes place.	At resonance, in parallel RLC circuit, current magnification takes place.	
11	The Q-factor for series resonant circuit is $Q = \frac{1}{R} \sqrt{\frac{L}{C}}$	The Q-factor for parallel resonant circuit is, $Q = \frac{1}{R} \sqrt{\frac{L}{C}}$	

•. С

4 Marks for any 6 points

 $\left(\frac{2}{3}\right)$ Marks for each point)



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12	Series RLC resonant	circuit is	Parallel RLC resonant circuit is	
12	Accepter circuit.		Rejecter circuit.	

2c) An alternating voltage of 250 V, 50 Hz is applied to a coil which takes 5A of current, the power absorbed by the circuit is 1 kW. Find the resistance and inductance of the coil.

Ans:

Given:
$$V = 250V$$
, $f = 50Hz$, $I = 5A$, $P = 1 \text{ kW}$

$$Z = \frac{V}{I} = \frac{250}{5} = 50 \Omega$$

$$P = VI \cos \emptyset$$

$$\therefore \cos \emptyset = \frac{P}{VI} = \frac{1 \times 10^3}{250 \times 5} = 0.8$$

$$\therefore \sin \emptyset = 0.6$$

$$P = 7 \cos \emptyset = 50 \times 0.8 = 400$$
1 Mark for R

$$R = Z \cos \emptyset = 50 \times 0.8 = 40\Omega$$

$$X_L = Z \sin \phi = 50 \times 0.6 = 30\Omega \qquad 1 \text{ Mark for } X_I$$

$$\therefore L = \frac{X_L}{2\pi f} = \frac{30}{2\pi \times 50} = 0.09549$$
henry = 95.49mH 1 Mark for L

Derive the expression for resonance frequency for R-L-C series circuit. 2d) Ans:

The frequency at which the net reactance of the series circuit becomes zero, is called the resonant frequency fr.

Its value can be found as under:

At resonance
$$X_L - X_C = 0$$
 or $X_L = X_C \quad \omega_r L = 1/\omega_r C$ 1 Mark

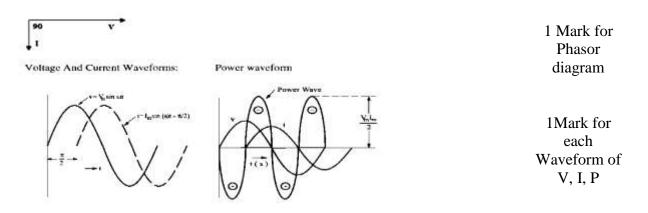
$$\omega_r^2 = 1/LC$$

... $(2 \pi f_r)^2 = LC$ 1 Mark

$$f_r = \frac{1}{2\pi\sqrt{LC}}$$
 1 Mark

2e) Draw the phasor diagram and waveforms of voltage, current and power in a pure inductance circuit supplied by a 1-phase a.c. source. Ans:

Phasor diagram and waveforms of purely inductive circuit:





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Compare series and parallel circuit. 2f) Ans:

Sr. No.	Series Circuit	Parallel Circuit
1	$ \begin{array}{c} 1 \\ \hline 1 \\ $	$\begin{array}{c} & 1 \\ & 1 \\ + \\ - \\ - \\ \end{array} \\ & V \\ R_1 \\ R_2 \\ R_3 \\ R$
2	A series circuit is that circuit in which the current flowing through each circuit element is same.	A parallel circuit is that circuit in which the voltage across each circuit element is same.
3	The sum of the voltage drops in series resistances is equal to the applied voltage V. \therefore V = V ₁ +V ₂ +V ₃	The sum of the currents in parallel resistances is equal to the total circuit current I. \therefore I = I ₁ +I ₂ +I ₃
4	$\therefore V = V_1 + V_2 + V_3$ The effective resistance R of the series circuit is the sum of the resistance connected in series. R = R ₁ + R ₂ + R ₃ +	The reciprocal of effective resistance R of the parallel circuit is the sum of the reciprocals of the resistances connected in parallel. $\frac{1}{R} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \cdots$
5	For series R-L-C circuit, the resonance frequency is, $f_r = \frac{1}{2\pi\sqrt{LC}}$	For parallel R-L-C circuit, the resonance frequency is, $f_r = \frac{1}{2\pi\sqrt{LC}}$
6	At resonance, the series RLC circuit behaves as purely resistive circuit	At resonance, the parallel RLC circuit behaves as purely resistive circuit.
7	At resonance, the series RLC circuit power factor is unity.	At resonance, the Parallel RLC circuit power factor is unity.
8	At resonance, the series RLC circuit offers minimum total impedance $Z = R$	At resonance, the parallel RLC circuit offers maximum total impedance Z =L/CR
9	At resonance, series RLC circuit draws maximum current from source, $I = (V/R)$	At resonance, parallel RLC circuit draws minimum current from source, $I = \frac{V}{[L/CR]}$
10	At resonance, in series RLC circuit, voltage magnification takes place.	At resonance, in parallel RLC circuit, current magnification takes place.
11	The Q-factor for series resonant circuit is	The Q-factor for parallel resonant circuit is,



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	$Q = \frac{1}{R} \sqrt{\frac{L}{C}}$	$Q = \frac{1}{R} \sqrt{\frac{L}{C}}$
12	Series RLC resonant circuit is Accepter circuit.	Parallel RLC resonant circuit is Rejecter circuit.

3 Attempt any **FOUR** of the following:

- A choke coil has a resistance of 2 Ω and an inductance of 0.035H is connected in 3a) parallel with a 350 μ F capacitor which is in series with a resistance of 20 Ω . When combination is connected across a 200V, 50Hz supply. Calculate:
 - The total current taken and i)
 - ii) Power factor of whole circuit

Ans:

Given:
$$R_1 = 2 \Omega$$
 $L = 0.035H$ $R_2 = 20 \Omega$ $C = 350 \mu F$
 $X_L = 2\pi fL = 2\pi \times 50 \times 0.035 = 11\Omega$
 $X_C = \frac{1}{2\pi fC} = \frac{1}{2\pi \times 50 \times 350 \times 10^{-6}} = 9.09\Omega$
^{1/2} Mark for X_C
^{1/2} Mark for X_C

$$\begin{array}{ll} Z_1 = R_1 + j \ X_L = (2 + j11) = 11.18 \angle 79.89^\circ \ \Omega & \frac{1}{2} \ \text{Mark for } Z_1 \\ Z_2 = R_2 - j \ X_C = (20 - j9.09) = 21.96 \angle -24.44^\circ \ \Omega & \frac{1}{2} \ \text{Mark for } Z_2 \\ \end{array}$$

$$I_1 = \frac{V}{Z_1} = \frac{200\angle 0^{\circ}}{11.18\angle 79.89^{\circ}} = 17.88\angle -79.89^{\circ} A = (3.138 - j17.60) A$$
^{1/2} Mark for I₁
^{1/2} Mark for I₂
^{1/2} Mark for I₂

Branch 2 current is given by, $I_2 = \frac{V}{Z_2} = \frac{200 \angle 0^{\circ}}{21.96 \angle -24.44^{\circ}} = 9.107 \angle 24.44^{\circ} \text{A} = (8.29 + j3.76) \text{ A}$ Total current is.

 $I = I_1 + I_2 = (3.138 - j17.60) + (8.29 + j3.76)$

I = (11.428 - j13.84)A = **17.94∠-50.45°** A i)

(I can be calculated by considering equivalent impedance also)

$$\cos \phi = \cos(-50.45^{\circ}) = 0.6368$$
 lagging

3b) A coil having resistance of 5Ω and inductance of 0.2H is arranged in parallel with another coil having resistance of 1Ω and inductance of 0.08H. Calculate the current through the combination and power absorbed when voltage of 100V, 50Hz is applied. Use impedance method.

Ans:

ii

Given: $R_1 = 5 \Omega$ $L_1 = 0.2H$ $R_2 = 1 \Omega$ $L_2 = 0.08H$ Branch 1 $X_{L1} = 2\pi f L_1 = 2\pi \times 50 \times 0.2 = 62.84 \Omega$ $Z_1^{II} = R_1 + jX_{L1} = 5 + j62.84 = 63.03 \angle 85.45^{\circ}\Omega$ ¹/₂ Mark Branch 2 $X_{L2} = 2\pi fL_2 = 2\pi \times 50 \times 0.08 = 25.136\Omega$ ¹/₂ Mark $Z_2 = R_2 + jX_{L2} = 1 + j25.136 = 25.155 \angle 87.72^{\circ}\Omega$

$$Z_{eq} = \frac{Z_1 \times Z_2}{Z_1 + Z_2} = \frac{(63.032/85.45^\circ) \times (25.1552/87.72^\circ)}{(5+j62.84) + (1+j25.136)} = \frac{1585.522/173.17^\circ}{6+j87.976}$$

1 Mark

¹/₂ Mark for I

¹/₂ Mark for pf

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3c)

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$=\frac{1585.52\angle 173.17^{\circ}}{88.18\angle 86.098^{\circ}}=17.98\angle 87.072^{\circ}\Omega$	
i) Current through the combination $I = \frac{V}{Z_{eq}} = \frac{100 \angle 0^{\circ}}{17.98 \angle 87.072^{\circ}} = 5.56 \angle -87.072^{\circ} \text{ amp}$	1 Mark
ii) Power absorbed $P = VI\cos\emptyset = 100 \times 5.56 \times \cos(-87.072^{\circ}) = 28.40W$	1 Mark
Define the following terms: (i) Leading quantity (ii) Lagging quantity	
Ans:	
Voltage Current	2 marks fo diagram
Capacitive circuit Leading Power factor Current is leading volatge	
When two alternating quantities attain their respective zero simultaneously, the quantities are said to be in-phase quantities.	or peak values

simultaneously, the quantities are said to be in-phase quantities.

When the quantities do not attain their respective zero or peak values simultaneously, then the quantities are said to be out-of-phase quantities.

Leading Quantity:

The quantity which attains the respective zero or peak value first, is called 1 Mark 'Leading Quantity'.

Lagging Quantity:

The quantity which attains the respective zero or peak value later, is called 'Lagging Quantity'.

In above diagram, it is seen that for inductive circuit, the current is lagging behind the voltage or the voltage is said to be leading the current.

Similarly, for capacitive circuit, the current is leading the voltage or the voltage is said to be lagging behind the current.



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3d) A RC series circuit consisting of R = 10 Ω , C = 100 μ F is connected across 200V, 50Hz AC supply. Find the value of current and power factor. What will be the value of current and power factor if the value of resistance is doubled? Ans: **Given:** $R = 10\Omega$, $C = 100\mu F$, V = 200V, f = 50 HzCapacitive reactance $X_{\rm C} = \frac{1}{2\pi fC} = \frac{1}{2\pi \times 50 \times 100 \times 10^{-6}} = 31.83\Omega$ 1 Mark for X_C **Circuit Impedance** $Z = R - jX_C = 10 - j31.83 = 33.36 \angle -72.55^{\circ}\Omega$ Current $I = \frac{V}{7} = \frac{200 \angle 0^{\circ}}{33.36 \angle -72.55^{\circ}} = 5.99 \angle 72.55^{\circ} \text{ amp}$ 1 Mark for current Power factor 1/2 Mark for $\cos \phi = \cos(72.55^\circ) = 0.299$ leading cosφ If the resistance is doubled, then $R = 20 \Omega$ Circuit Impedance $Z = R - iX_{C} = 20 - i31.83 = 37.59 \angle -57.86^{\circ}\Omega$ 1 Mark for Current current $I = \frac{V}{Z} = \frac{200 \angle 0^{\circ}}{37.59 \angle -57.86^{\circ}} = 5.32 \angle 57.86^{\circ} \text{ amp}$ 1/2 Mark for Power factor cos¢ $\cos \phi = \cos(57.86^\circ) = 0.532$ leading A 200W, 100V lamp is connected in series with a capacitor to a 120V, 50Hz AC 3e) supply. Calculate: i) The capacitance required ii) The phase angle between voltage and current Ans: **Given:** Power rating of lamp P = 200WRated voltage of lamp $V_R = 100V$ Supply voltage V = 120V, Frequency f = 50Hz1) Capacitance required: The value of capacitance should be such that rated voltage of 100V appears across the lamp and lamp consumes rated power of 200W. 1 Mark for R $\therefore \text{ Resistance of lamp } R = \frac{V_R^2}{P} = \frac{100^2}{200} = 50\Omega$ $P = \frac{V_R^2}{R}$ For rated power consumption in lamp, the required current be, $I = \frac{V_R}{R} = \frac{100}{50} = 2 \text{ amp}$ 1 Mark for Z Impedance required to draw this current from 120V supply, $Z = \frac{V}{I} = \frac{120}{2} = 60\Omega$ Capacitive reactance $X_C = \sqrt{(Z^2 - R^2)}^2 = \sqrt{(60^2 - 50^2)} = 33.16\Omega$ Capacitance $C = \frac{1}{2\pi f X_C} = \frac{1}{2\pi \times 50 \times 33.16} = 100.54 \mu F$ 1 Mark for C Power loss in the circuit $P = VIcos \emptyset$



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$\therefore power factor \cos \phi = \frac{P}{VI} = \frac{200}{120 \times 2} = 0.833$ Phase angle between voltage and current, $\phi = \cos^{-1}(0.833) = 33.59^{\circ}$	1 Mark fo
 State the relation between line and phase values of current and voltage for and delta connection. Ans: 	star
Star Connection: Line Voltage = $\sqrt{3}$ (Phase Voltage)	1 Mark
i.e $V_L = \sqrt{3}V_{ph}$ Line Current = Phase Current i.e $I_L = I_{ph}$	1 Mark
Delta Connection: Line Voltage = Phase Voltage	1 Mark
i.e $V_L = V_{ph}$ Line Current $= \sqrt{3}$ (Phase Current) i.e $I_L = \sqrt{3}I_{ph}$	1 Mark
Attempt any <u>FOUR</u> of the following:	16
 a) Three coils each with resistance of 10Ω and inductance of 0.35mH are connering star to a 3-phase, 440V, 50Hz supply. Calculate the line current and power taken per phase. Ans: Data Given: 	
Line voltage $V_L = 440V$, $f = 50Hz$ Resistance per phase $R = 10\Omega$ Inductance per phase $L = 0.35mH$ Inductive reactance per phase	
$X_{L} = 2\pi fL = 2\pi \times 50 \times 0.35 \times 10^{-3} = 0.11\Omega$ $Z_{ph} = (10+j0.11) \Omega = 10.0006 \angle 0.63^{\circ} \Omega$ In star connected load $V_{L} = \sqrt{3} Vph$ and $I_{L} = I_{Ph}$ $V_{ph} = \frac{V_{L}}{\sqrt{3}} = \frac{440}{\sqrt{3}} = 254.03V$	1 Mark
(i) Line Current $I_L = I_{ph} = \frac{V_{ph}}{Z_{ph}} = \frac{254.03 \angle 0^\circ}{10 \angle 0.63^\circ}$ = 25.40 $\angle -0.63^\circ A$	1 Mark
(ii) Power factor $\cos\phi = \frac{Rph}{Zph} = \frac{10}{10} = 0.999$ (iii) Power taken per phase P = V _{Ph} I _{Ph} $\cos\phi$	1 Mark
= (254.03)(25.4)(0.999) $= 6445.909 W$	1 Mark
b) State any four advantages of polyphase circuit over single phase circuit.	
Ans: Advantages of Polyphase circuit over Single phase circuit:	

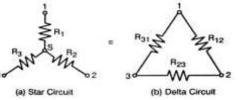
Advantages of Polyphase circuit over Single phase circuit: i) Three-phase transmission is more economical than single-phase transmission. It requires less copper material.



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- ii) Parallel operation of 3-phase alternators is easier than that of single-phase alternators.
- iii) Single-phase loads can be connected along with 3-ph loads in a 3-ph system.
- iv) Instead of pulsating power of single-phase supply, constant power is obtained in 3-phase system.
- v) Three-phase induction motors are self-starting. They have high efficiency, better power factor and uniform torque.
- vi) The power rating of 3-phase machine is higher than that of 1-phase machine of the same size.
- vii) The size of 3-phase machine is smaller than that of 1-phase machine of the same power rating.
- viii) Three-phase supply produces a rotating magnetic field in 3-phase rotating machines which gives uniform torque and less noise.
- 4 c) Derive the formulae for delta and star transformation.
 - Ans:

i) Star to Delta Transformation:



If the star circuit and delta circuit are equivalent, then the resistance between any two terminals of the circuit must be same.

For star circuit, resistance between terminals 1 & 2, say $R_{1-2} = R_1 + R_2$ For delta circuit, resistance between terminals 1 & 2, $R_{1-2} = R_{12}||(R_{31} + R_{23})$ $\therefore R_1 + R_2 = R_{12}||(R_{31} + R_{23}) = \frac{R_{12}(R_{31} + R_{23})}{R_{12} + (R_{31} + R_{23})} = \frac{R_{12}(R_{31} + R_{23})}{R_{12} + R_{23} + R_{31}}$

Similarly, the resistance between terminals 2 & 3 can be equated as,

 $\therefore R_{2} + R_{3} = \frac{R_{12}R_{23} + R_{23}R_{31}}{R_{12} + R_{23} + R_{31}} \dots (2)$ And the resistance between terminals 3 & 1 can be equated as, $\therefore R_{3} + R_{1} = \frac{R_{23}R_{31} + R_{12}R_{31}}{R_{12} + R_{23} + R_{31}} \dots (3)$ Subtracting eq. (2) from eq.(1), $\therefore R_{1} - R_{3} = \frac{R_{12}R_{31} - R_{23}R_{31}}{R_{12} + R_{23} + R_{31}} \dots (4)$ Adding eq.(3) and eq.(4) and dividing both sides by 2, $\therefore R_{1} = \left[\frac{R_{12}R_{31}}{R_{12} + R_{23} + R_{31}}\right] \dots (5)$ Similarly, we can obtain, $\therefore R_{2} = \left[\frac{R_{12}R_{23}}{R_{12} + R_{23} + R_{31}}\right] \dots (6)$ 1 Mark for each of any 4 advantages = 4 Marks

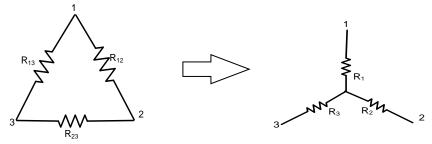
2 Marks for star to delta conversion



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Thus using known star connected resistors R_1 , R_2 and R_3 , the unknown resistors R_{12} , R_{23} and R_{31} of equivalent delta connection can be determined.

ii) Delta to Star transformation:



Delta connection Equivalent star connection R_{12} , R_{23} and R_{32} connected in delta fashion between terminals 1, 2 and 3. It is possible to replace delta by its equivalent star circuit. Considering terminals 1 and 2, Resistance R_{12} appears in parallel with ($R_{23}+R_{31}$) Hence resistance between terminals 1 and 2

$$\frac{R_{12}(R_{23}+R_{31})}{R_{12}+R_{23}+R_{31}}$$
.....(1)



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In Case of Star network, resistance between terminals 1 and 2 is $= R_{1} + R_{2} \dots \dots \dots (2)$ For equivalence between two networks, equating Equation (1) & (2) $R_{1} + R_{2} = \frac{R_{12}(R_{23} + R_{31})}{R_{12} + R_{23} + R_{31}} \dots \dots (3)$ Similarly, we can write: $R_{2} + R_{3} = \frac{R_{23}(R_{31} + R_{12})}{R_{12} + R_{23} + R_{31}} \dots \dots (4)$ $R_{3} + R_{1} = \frac{R_{31}(R_{12} + R_{23})}{R_{12} + R_{23} + R_{31}} \dots \dots (5)$ By subtracting equation (4) from (3) $R_{1} - R_{3} = \frac{R_{12}R_{23} + R_{12}R_{31} - R_{23}R_{31} - R_{23}R_{12}}{R_{12} + R_{23} + R_{31}} \dots \dots (6)$ By adding equation (5) & (6) $2R_{1} = \frac{R_{31}R_{12} + R_{31}R_{23} + R_{12}R_{31} - R_{23}R_{31}}{R_{12} + R_{23} + R_{31}}$ Equivalent star resistances for delta connection are then given by, $R_{1} = \frac{R_{12}R_{31}}{R_{12} + R_{23} + R_{31}}$ $R_{1} = \frac{R_{12}R_{31}}{R_{12} + R_{23} + R_{31}}$

$$R_{2} = \frac{R_{12}R_{23}}{R_{12} + R_{23} + R_{31}}$$
$$R_{3} = \frac{R_{23}R_{31}}{R_{12} + R_{23} + R_{31}}$$

4 d) A delta connected induction motor is supplied by 3-phase, 400V, 50Hz supply the line current is 43.3A and the total power taken from the supply is 24kW. Find the resistance and reactance per phase of motor winding.

f = 50Hz

Ans:

- Data Given:
- Line voltage $V_L = V_{ph} = 400V$, Line current $I_L = 43.3A$
- Total power P = 24kWP = $\sqrt{3}V_{e}I_{e}$

$$= \sqrt{3} V_L I_L \cos \emptyset$$

$$\therefore \quad \cos \emptyset = \frac{P}{\sqrt{3} V_L I_L} = \frac{(24 \times 10^3)}{\sqrt{3} \times 400 \times 43.3} = 0.8 \log 1 \text{ Mark}$$

Thus, $\sin \phi = 0.6$

$$I_{ph} = \frac{I_L}{\sqrt{3}} = \frac{43.3}{\sqrt{3}} = 25amp$$

$$Z_{ph} = \frac{V_{ph}}{I_{ph}} = \frac{400}{25} = 16\Omega$$
1 Mark

Resistance per phase

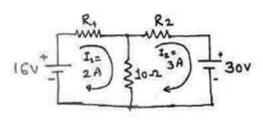
$$R = Z\cos\phi = 16 \times 0.8 = 12.8\Omega$$
 1 Mark

 $X = Z\sin\phi = 16 \times 0.6 = 9.6\Omega$ 1Mark



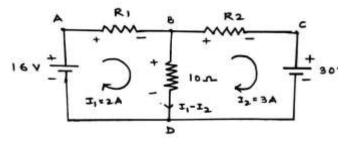
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4 e) Using mesh analysis find values of R_1 and R_2 shown in Figure No. 2





Ans:



By applying KVL to loop ABDA

$$-I_1R_1 - 10(I_1 - I_2) + 16 = 0$$
 1 Mark
 $-2R_1 - 10(2 - 3) + 16 = 0$
 $-2R_1 + 10 + 16 = 0$
 $-2R_1 = -26$
 $R_1 = 13\Omega$ 1 Mark
By applying KVL to loop BCDB

By applying KVL to loop BCDB

$$-I_2R_2 + 10(I_1 - I_2) - 30 = 0$$
 1 Mark
 $-3R_2 - 10 - 30 = 0$

$$3R_2 = -40$$

 $R_2 = -13.33\Omega$ 1 Mark

Derive the condition for maximum power transfer theorem.

Ans:

4 f)

Condition for maximum power transfer theorem:

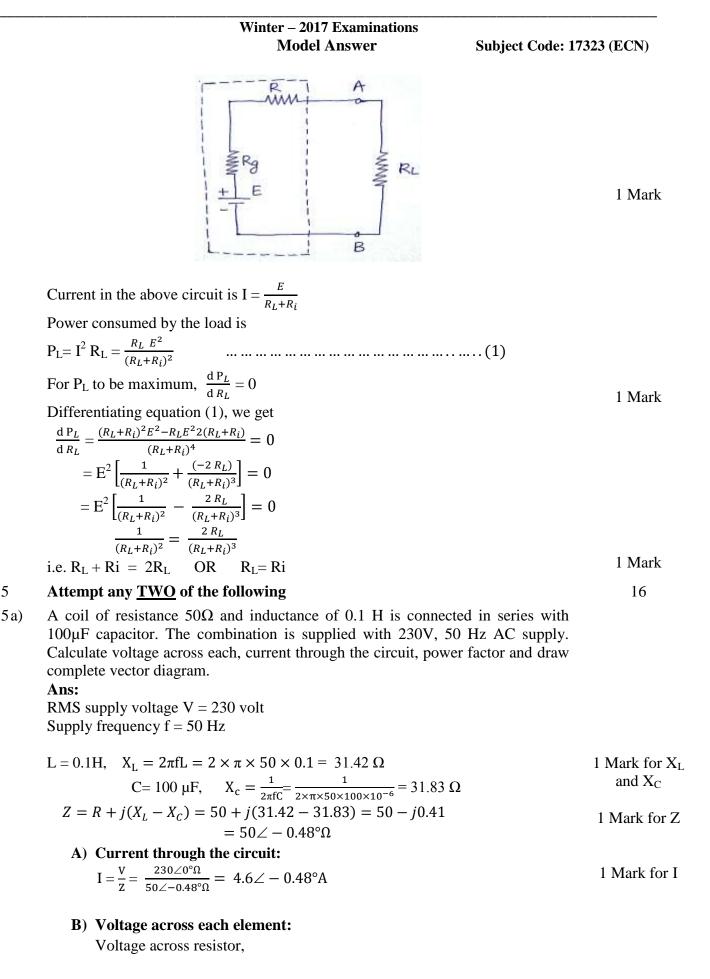
Maximum power transfer theorem states that, resistive load will absorb maximum power from a network when the load resistance is equal to the resistance of the network as viewed from the output terminals, with all energy sources removed leaving behind their internal resistances.

In Fig, a load resistance of R_L is connected across the terminals A and B of a network which consists of a generator of e.m.f. E and internal resistance Rg and a series resistance R which represents the lumped resistance of the connecting wires.

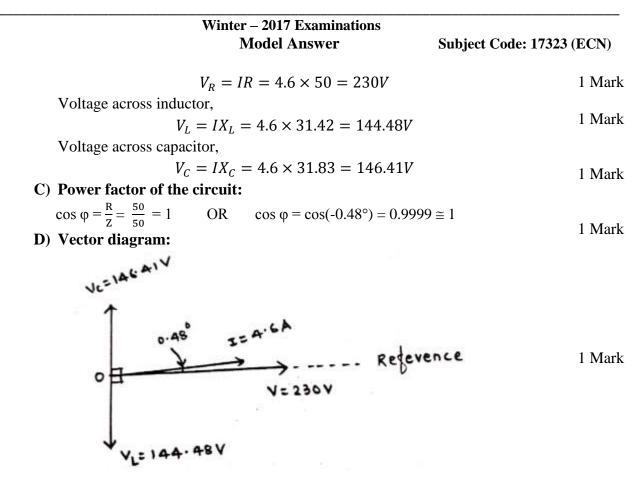
Let Ri = Rg + R = internal resistance of the network as viewed from A and B. According to this theorem, R_L will absorb maximum power from the network when $R_L = R_i$.



5

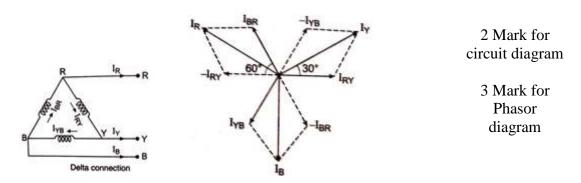






5b) With the help of necessary phasor diagram, derive the relationship between line and phase current in balanced delta connected load connected to 3 phase A.C. supply.

Ans:



From above diagram current in each lines are vector difference of the two phase currents flowing through that line. For example:

Current in line R is
$$I_R = I_{BR} - I_{RY}$$

Current in line Y is $I_Y = I_{RY} - I_{YB}$
Current in line B is $I_B = I_{YB} - I_{BR}$
1 Mark

Current in line R is found by compounding I_{BR} and I_{RY} and value given by parallelogram in phasor diagram. Angle between I_{BR} and $-I_{RY}$ is 60°, where $|I_{BR}| = |I_{RY}| =$ Phase current I_{ph}



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$$I_{R} = I_{BR} - I_{RY} = 2I_{ph} \cos\left(\frac{60}{2}\right) = 2I_{ph}\frac{\sqrt{3}}{2} = \sqrt{3}I_{ph}$$

$$I_{Y} = I_{RY} - I_{YB} = 2I_{ph} \cos\left(\frac{60}{2}\right) = 2I_{ph}\frac{\sqrt{3}}{2} = \sqrt{3}I_{ph}$$

$$I_{B} = I_{YB} - I_{BR} = 2I_{ph} \cos\left(\frac{60}{2}\right) = 2I_{ph}\frac{\sqrt{3}}{2} = \sqrt{3}I_{ph}$$
As $I_{R} = I_{Y} = I_{B} = I_{L}$

$$I_{L} = \sqrt{3}I_{ph}$$

5c) i) State Thevenin's theorem and write its procedural steps to find current in a branch. (Assume simple circuit)

Ans:

Thevenin's Theorem:

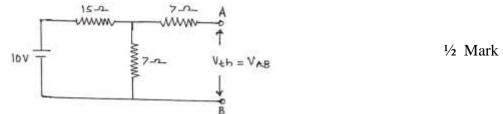
Any two terminal circuit having number of linear impedances and sources (voltage, current, dependent, independent) can be represented by a simple equivalent circuit consisting of a single voltage source V_{Th} in series with an impedance Z_{Th} , where the source voltage V_{Th} is equal to the open circuit voltage appearing across the two terminals due to internal sources of circuit and the series impedance Z_{Th} is equal to the impedance of the circuit while looking back into the circuit across the two terminals, when the internal independent voltage sources are replaced by short-circuits and independent current sources by open circuits.

Procedural steps to find current in a branch using Thevenin's theorem: Consider a simple circuit shown below in which we need to find the current flowing through 10Ω resistor.



Step I: Identify the load branch: It is the branch whose current is to be determined.

Step II: Calculation of V_{Th} : Remove R_L and find open circuit voltage across the load terminals A and B.



Current through circuit will be =10/(15+7)=0.45 Amp $V_{OC} = V_{Th} = V_{AB} = 0.45 \text{ x} 7 = 3.18 \text{ V}$

1⁄2 Mark

Step III: Calculation of R_{Th}:



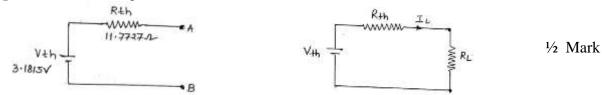
1 Mark



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Resistances 15 & 7 are in parallel =15 x 7/(15+7)=4.77 Ω R_{Th}= 7+4.77=11.77 Ω

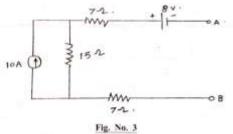
Step IV: Thevenin's equivalent circuit:



Step V: Determination of Load current:

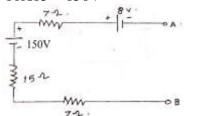
 $I_L = V_{Th}/(R_{Th}+R_L)=3.18/(11.77+10)=0.146 \text{ Amp}$

5c) ii) Develop Thevenin's equivalent across A and B in network shown below in Figure No. 3



Ans:

1) Converting current source of 10A with 15Ω as internal resistance into voltage source V = 10X15 = 150V



1 Mark

1/2 Mark

2) Determination of Thevenin's equivalent voltage V_{Th}: Due to open circuit between A & B, the current is zero and voltage drop across all resistors is zero. The open circuit voltage between A & B can be obtained by KVL as,

$$\begin{split} V_{AB} &= 7(0) + 15(0) + 150 + 7(0) - 8 = 142 \\ \therefore \ V_{Th} &= V_{AB} = 142 V \end{split}$$
 1 Mark

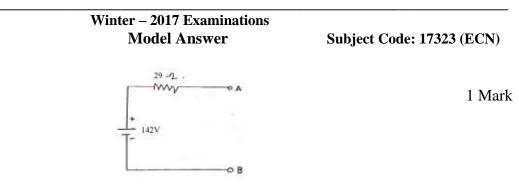
3) Determination of Thevenin's equivalent resistance R_{Th}:



 $R_{Th}=7+15+7=29\Omega$

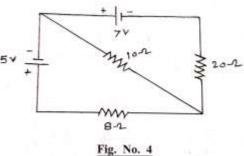
4) Thevenin's Equivalent Circuit:





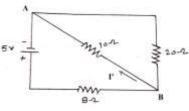
6 Attempt any <u>FOUR</u> of the following

6a) Calculate current through 10Ω resistance in the network shown in Figure No. 4 using superposition theorem.

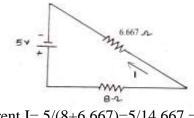


Ans:

A) Consider 5 V source only:

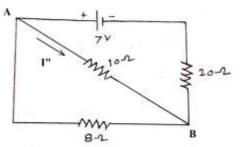


Resistances of 10 & 20 are in parallel = $10 \times 20/(10+20) = 6.667\Omega$



Total current I= 5/(8+6.667)=5/14.667=0.341 amp Therefore I'= $0.341 \times 20/(10+20) = 0.227$ amp I' = 0.227 amp from B to A

B) Now consider 7 V source only:



Resistances of 10 & 8 are in parallel= $10 \times 8/(10+8) = 4.44\Omega$

1 Mark

1 Mark

16



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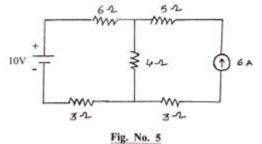
+ + 7V 1 Mm 4.44 2 4.44 2

Therefore current I =7/(20+4.44) = 0.286 amp I" = $0.286 \times 8/(10+8) = 0.127$ amp from A to B

C) Final Current:

I = I' - I'' from B to A	OR	$I = I^{\prime\prime} - I^{\prime}$ from A to B	l Mark
= 0.227 - 0.127 = 0.099 a	-		1 Mark

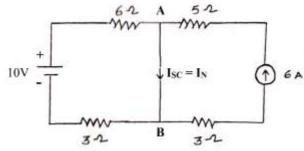
6b) Using Norton's theorem, find current through 4Ω resistance in Figure No. 5



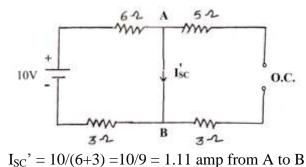
Ans:

Here load branch is 4Ω , hence $R_L = 4\Omega$

A) Determination of Norton's Equivalent Current Source I_N : Remove R_L and short the path, now circuit becomes as shown below

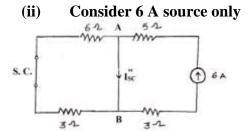


 $\begin{array}{c} \mbox{Apply Superposition theorem to find out the } I_{SC} = I_N \\ \mbox{(i)} \quad \mbox{Consider 10 V source only} \end{array}$





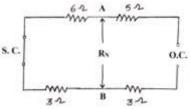
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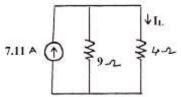
1 Mark

$$\begin{split} I_{SC}" &= 6 \text{ amp from A to B} \\ I_{SC} &= I_N \!=\! I_{SC}" + I_{SC}" = 1.11 + 6 \\ I_N &= I_{SC} = 7.11 \text{ amp from A to B} \end{split}$$

B) Determination of Norton's Equivalent Resistance R_N : Now calculate $R_N = R_{Th}$

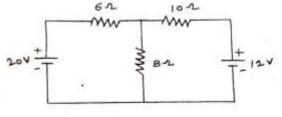


 $R_N = R_{AB} = 6+3 = 9 \Omega$ Norton's equivalent circuit becomes



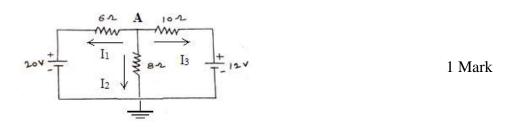
Therefore current through R_L is $I_L = 7.11 \times 9/(9+4)$ $I_L = 4.922 \text{ amp}$

6c) Find current through 8Ω resistance using nodal analysis in Figure No. 6.





Ans:

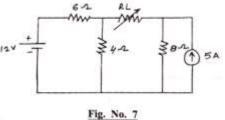


1Mark



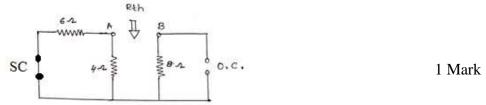
Winter – 2017 Examinations Model Answer Subject Code: 17323 (ECN) By applying KCL to Node A $I_1+I_2+I_3=0$ 1 Mark $\frac{V_A - 20}{6} + \frac{V_A}{8} + \frac{V_A - 12}{10} = 0$ 1 Mark $\frac{8(V_A - 20) + 6V_A}{6 \times 8} + \frac{V_A - 12}{10} = 0$ 1 $\frac{14V_A - 160}{6 \times 8} + \frac{V_A - 12}{10} = 0$ 0 $140V_A - 1600 + 48V_A - 576 = 0$ 1 $188V_A = 2176$ 1 $V_A = 11.57$ volts 1 Current flowing through resistance $8 \Omega = \frac{V_A}{8} = 1.446$ Amp 1 Find the value of P. to transfer maximum never in the nature k chosen in Figure 1

6d) Find the value of R_L to transfer maximum power in the network shown in Figure No. 7

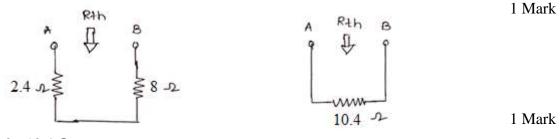


Ans:

Maximum power will be transferred when load resistance is equal to internal resistance i.e. $R_L = R_{TH}$



Resistances of 6 & 4 are in parallel = $6 \times 4/(6+4)$ = 2.4 Ω and circuit is simplified as



 $\label{eq:RTH} \begin{array}{l} R_{TH} = 2.4 + 8 = 10.4 \ \Omega \\ \\ \text{Hence in the given circuit maximum power will be transferred when} \\ R_L = R_{th} = 10.4 \Omega \end{array} \begin{array}{l} 1 \ \text{Mark} \end{array}$



Subject Code: 17323 (ECN)

6e) Explain concept of initial and final conditions in switching circuits. For the elements R, L and C.

Ans:

Concept of initial and final conditions:

For the three basic circuit elements the initial and final conditions are used in following way:

i) Resistor:

At any time it acts like resistor only, with no change in condition.

ii) Inductor:

<u>The current through an inductor cannot change instantly.</u> If the inductor current is zero just before switching, then whatever may be the applied voltage, just after switching the inductor current will remain zero. i.e the inductor must be acting as open-circuit at instant t = 0. If the inductor current is I₀ before switching, then just after switching the inductor current will remain same as I₀, and having stored energy hence it is represented by a current source of value I₀ in parallel with open circuit.

As time passes the inductor current slowly rises and finally it becomes 1 Mark constant. Therefore the voltage across the inductor falls to $zero \left[v_L = L \frac{di_L}{dt} = \right]$

0. The presence of current with zero voltage exhibits short circuit condition.

Therefore, under steady-state constant current condition, the inductor is represented by a short circuit. If the initial inductor current is non-zero I_0 , making it as energy source, then finally inductor is represented by current source I_0 in parallel with a short circuit.

iii) Capacitor:

<u>The voltage across capacitor cannot change instantly.</u> If the capacitor voltage is zero initially just before switching, then whatever may be the current flowing, just after switching the capacitor voltage will remain zero. i.e the capacitor must be acting as short-circuit at instant t = 0. If capacitor is previously charged to some voltage V_0 , then also after switching at t = 0, the voltage across capacitor remains same V_0 . Since the energy is stored in the capacitor, it is represented by a voltage source V_0 in series with short-circuit. As time passes the capacitor voltage slowly rises and finally it becomes constant. Therefore the current through the capacitor falls to zero $[i_c = C \frac{dv_c}{dt} = 0]$. The presence of voltage with zero current exhibits open circuit condition. Therefore, under steady-state constant voltage condition, the capacitor is represented by a open circuit. If the initial capacitor voltage is non-zero V_0 , making it as energy source, then finally capacitor is represented by voltage source V_0 in series with an open-circuit.

The initial and initial conditions are summarized in following table.			
Element and condition	Initial Condition at	Final Condition at	
at	$t = 0^+$	$t = \infty$	
$t = 0^{-1}$			
R	R	R	
°-///~°	~~~~~	°-WW	
L	0.C.	S.C.	
•	0 0 0 0	00	

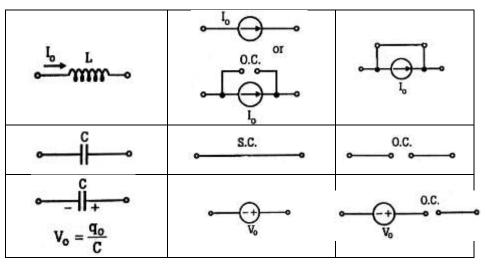
The initial and final conditions are summarized in following table:

1 Mark For table

1 Mark



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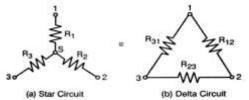


6f) Give the expression for star to delta and delta to star transformation. **Ans:**

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1 Mark

Star to Delta Transformation:

$$R_{12} = R_1 + R_2 + \frac{R_1 R_2}{R_3}$$

$$R_{23} = R_2 + R_3 + \frac{R_2 R_3}{R_1}$$

$$R_{31} = R_3 + R_1 + \frac{R_3 R_1}{R_2}$$
^{1/2} Mark for each equation
= 1^{1/2} Marks

Delta to Star Transformation:

$$R_{1} = \frac{R_{12}R_{31}}{R_{12} + R_{23} + R_{31}}$$

$$R_{2} = \frac{R_{12}R_{23}}{R_{12} + R_{23} + R_{31}}$$

$$R_{3} = \frac{R_{23}R_{31}}{R_{12} + R_{23} + R_{31}}$$

$$R_{3} = \frac{R_{23}R_{31}}{R_{12} + R_{23} + R_{31}}$$

$$R_{3} = \frac{R_{3}R_{31}}{R_{12} + R_{23} + R_{31}}$$

$$R_{3} = \frac{R_{3}R_{31}}{R_{12} + R_{23} + R_{31}}$$